

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10982103-1

SERIAL NO.

10982103-1
JC598 U.S. PTO
01/18/00
01/18/00
JC598 U.S. PTO
01/18/00
JC598 U.S. PTO
01/18/00

APPLICANT

D. Amnon Silverstein

FILING DATE

Jan. 18, 2000

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
MLH	1A	5,138,460	08/11/92	Akira Egawa	358	224
MLH	1B	5,196,929	03/23/93	Tetsuo Miyasaka	358	125
MLH	1C	5,251,037	10/05/93	Stavros Busenberg	358	213.28
MLH	1D	5,270,756	12/14/93	Stavros Busenberg	358	109
MLH	1E	5,808,678	09/15/98	Yuji Sakaegi	348	334
MLH	1F	5,786,804	07/28/98	Gary Gordon	345	158
MLH	1G	5,825,350	10/20/98	Charles Case, Jr.	345	163
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
MLH	1L	0 734 155 A1	12/05/95	Hiroshi Aramaki	5/232	—	X	✓
MLH	1M	0 884 890 A1	04/28/98	Stephen Pollard	1/047	—	X	✓
MLH	1N	0 884 897 A1	06/08/98	Takafumi Miyatake	5/232	—	X	✓
MLH	1O	WO 99/12341	03/11/99	M. Akasawa	1/21	—		✓
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Data Sheet for a QVGA Display Module, Model No. HMW3-0076
1R	Data Sheet for High-Performance SVGA Microdisplay, Model No. MDB00G6
1S	

EXAMINER

Matthew L. Hausele

DATE CONSIDERED

10/10/03